•	Application No.	Applicant(s)
Notice of Allowability	09/600,890	SHIRAI ET AL.
	Examiner	Art Unit
	Tuan T Dinh	2841
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication IGHTS. This application is subject to	olication. If not included will be mailed in due course. THIS
1. This communication is responsive to <u>06/18/04</u> .		
2. The allowed claim(s) is/are 1-3 and 5-1012.		
3. The drawings filed on 16 August 2000 are accepted by the Examiner.		
<ul> <li>4.  Acknowledgment is made of a claim for foreign priority una)  All b)  Some* c)  None of the:  1.  Certified copies of the priority documents have 2.  Certified copies of the priority documents have 3.  Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:  Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.</li> <li>5.  A SUBSTITUTE OATH OR DECLARATION must be submanted in INFORMAL PATENT APPLICATION (PTO-152) which give 6.  CORRECTED DRAWINGS (as "replacement sheets") must (a)  including changes required by the Notice of Draftspers 1)  hereto or 2)  To Paper No./Mail Date  (b)  including changes required by the attached Examiner's Paper No./Mail Date  Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the deponant of the proper No./Mail Date  DEPOSIT OF and/or INFORMATION about the deponant attached Examiner's comment regarding REQUIREMENT.</li> </ul>	been received.  been received in Application Nocuments have been received in this recei	complying with the requirements  S AMENDMENT or NOTICE OF tion is deficient.  948) attached  ffice action of the back) of di.  nust be submitted. Note the
Attachment(s)  1. ☐ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	6. ⊠ Interview Summary Paper No./Mail Dat 8), 7. ⊠ Examiner's Amendr	e <u>09/08/04</u> .

,t'

Art Unit: 2841

## **DETAILED ACTION**

## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Hong Xu on September 08, 2004.

The application has been amended as follows to overcome the references cited.

Cancel claims 18 and 28 without prejudice.

Claim 17, line 9, after "electroplating layer" please, insert --; wherein depressions are formed in the central surface portion of the plating layer surface extending out of the through holes--.

Claim 25, line 7, after "thermal plastic resin" please, insert --; wherein depressions are formed in the central surface portion of the plating layer surface extending out of the through holes--.

Application/Control Number: 09/600,890

Art Unit: 2841

## Allowable Subject Matter

2. Claims 1-3, 5-10, 12, 14-17, 19, 21-27, and 29-36 are allowed.

The following is an examiner's statement of reasons for allowance: the references cited disclose a multiplayer printed circuit board comprising conductor circuit layers, interlaminate insulative resin layers, through holes each having an inner wall, and some other claimed elements, for example, Uno et al. (US. Patent 5,827,604) discloses a multiplayer printed circuit board comprising conductor layers (3) each having a thickness, and interlaminate insulative resin layers (4) each having a through hole (7 or 11) including an inner wall, the inner wall filled up with a plating layer. Frankeny et al. (US Patent 5,509,200) discloses a multiplayer printed circuit board as shown in figure 9 comprising dendrites (21) filled in inner walls of through holes (13). However, they do not disclose or render obvious in combination of the through holes, each having the inner wall filled up with the plating layer having a substantially flat surface to form a via hole, and the surface of the plating layer that extends out of the through hole and lies in a substantially same level as the surface of the conductor circuit layer (as recited in claim 1), and depressions are formed on a central surface portion of the plating layer extending out of the through hole (as recited in claims 9, 17, and 25). Also, there is no suggestion to modify these limitations as claimed as above.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T Dinh whose telephone number is 571-272-1929. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kammie Cuneo can be reached on 571-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tuan Dinh September 08, 2004.

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800